

**REMARKS**

The indicated allowability over the art of claims 4-12, 17-25 and 43 is noted, with thanks. However, Applicants believe that all of the pending claims are allowable over the art.

Before considering the art rejection in detail, it should be noted, the present invention has as a feature that charges of the gradation pixel data written in the holding capacitor are discharged through the drive transistor. Due to this feature, the present invention has advantageous effect that the variation in characteristics of the drive transistor is compensated to reduce the variation in luminance.

On the other hand, the primary reference Dingwall only teaches the feature that write operation is carried out for a holding capacitor when a selection transistor is turned on. If the gradation pixel data written in the holding capacitor is discharged in Dingwall, this discharge is caused by a leak current of the selection transistor or the like. In Dingwall, it is not possible to compensate for the variation in characteristics in the drive transistor.

The newly cited U.S. patent publication to Tanaka et al. (Tanaka) does not teach the missing teachings to Dingwall.

Therefore, no combination of Dingwall and Tanaka reasonably could be said to teach or suggest any of claims 1, 14 and 45 or claims 2, 3, 12, 13, 15, 16 or 26 which depend therefrom as the case may be.

Turning to the rejection of claim 44 as obvious from Dingwall in view of Ikeda, the deficiencies of Dingwall are discussed above. Ikeda does not supply the missing teachings. Ikeda teaches the constitution in which an organic EL element is connected to a drive transistor in parallel. The cathode of the organic EL element is connected to a capacitor and

the source of the drive transistor while the anode of the EL elements is connected to the drain of the drive transistor. On the contrary, the pixel display element of the present claimed invention is serially connected to the drive transistor. Therefore, in one arrangement of the present claimed invention, the anode of the EL element is connected to the drive transistor and one end of the capacitor while the cathode is connected to a ground line (GND), and the other end of the capacitor. In another arrangement of the present claimed invention, the cathode of the EL element is connected to the drive transistor and one end of the capacitor while the anode is connected to a power supply line (Vcc) and the other end of the capacitor. The present claimed invention recited in claim 44 is clearly different from Ikeda in the connection relationship between the organic EL element and the drive transistor.

The current flowing through the EL element 20 is supplied from a constant current source 24 in Ikeda. The drive transistor 22 does not supply a current to the EL element 20. In the present invention, the drive current supplies the current to the EL element.

In addition, the node among the drive transistor 22, organic EL element 20 and capacitor 26 of each pixel of Ikeda is connected to the common electrode which is provided in common with respect to the plurality of pixels. In the present invention, each pixel has its own node among the drive transistor, the organic EL element and the capacitor. The above advantageous effect of the present invention is not suggested by the combination of Dingwall and Ikeda.

Therefore, no combination of Dingwall and Ikeda reasonably could be said to achieve or render obvious claim 44, and claim 44 cannot be said to be obvious from Dingwall in view of Ikeda.

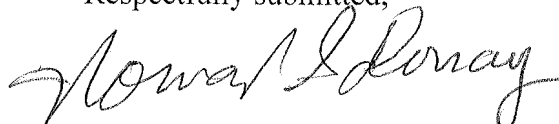
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Having dealt with all the objections raised by the Examiner, the application is believed to be in order for allowance.

In the event further fees or charges are due, the Commissioner is authorized to charge our deposit account (or credit any overpayment) to our Deposit Account Number 08-1391.

Respectfully submitted,

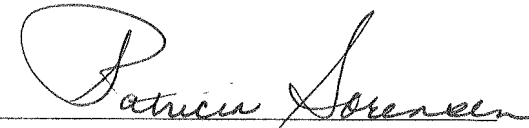


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I hereby certify that this correspondence is being deposited with the United States Patent Office via the electronic filing procedure on December 10, 2009.

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